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Docket No.: 219713US2

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313



RE: Application Serial No.: 10/075,464 ✓

Applicants: Shigeru KAWANAKA

Filing Date: February 15, 2002

For: SEMICONDUCTOR MEMORY DEVICE AND ITS
MANUFACTURING METHOD

Group Art Unit: 2826

Examiner: Kevin QUINTO

SIR:

Attached hereto for filing are the following papers:

RESPONSE TO RESTRICTION REQUIREMENT

Our check in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

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219713US2



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

Shigeru KAWANAKA

: EXAMINER: Kevin QUINTO

SERIAL NO: 10/075,464

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FILED: February 15, 2002

: GROUP ART UNIT: 2826

FOR: SEMICONDUCTOR MEMORY
DEVICE AND ITS MANUFACTURING
METHOD

RESPONSE TO RESTRICTION REQUIREMENT

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Official Action mailed June 27, 2003, Applicant elects Group I,
Claims 1-15 for further examination on the merits in the present application.

Accordingly, an action on the merits is respectfully requested.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

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